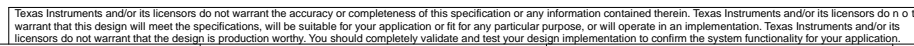



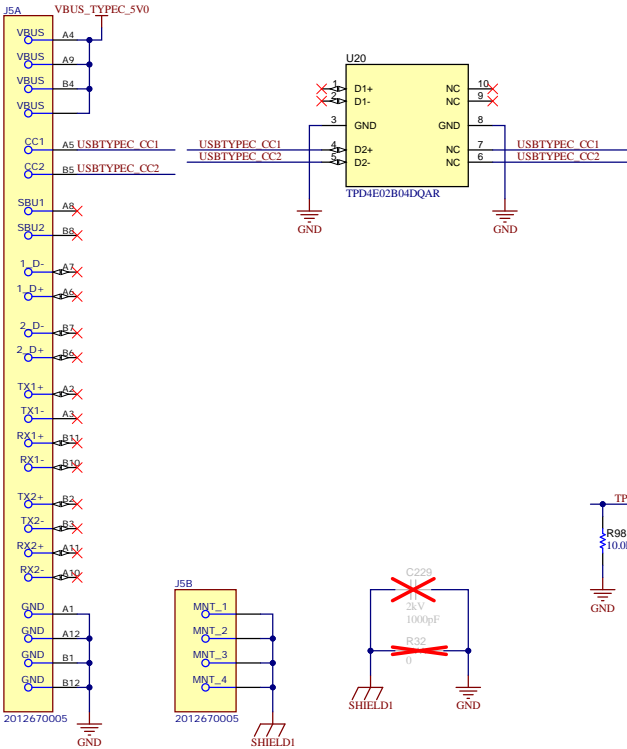
System Block Diagram



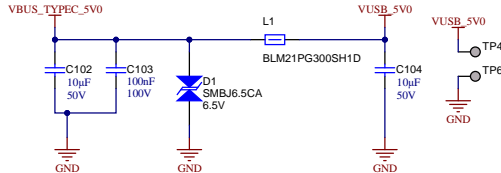
Orderable: LP-AM263		Designed for:	Mod. Date: 11/30/2024	 TEXAS INSTRUMENTS http://www.ti.com © Texas Instruments
TID #: N/A		Project Title: AM263x Launchpad		
Version: PROC-111 [Rev: A]		Sheet Title:		
01	S/NR: N/A Not in version control	Assembly Variant: 001		
Drawn By: a0271760		File: PROC-111_CoverSheet_SchDoc	Sheet: 1 of 24	
Engineer: a0271760		Contact:	Size: B	

TPS62913 Datasheet
TPS22965 Datasheet
TPS62913 Datasheet

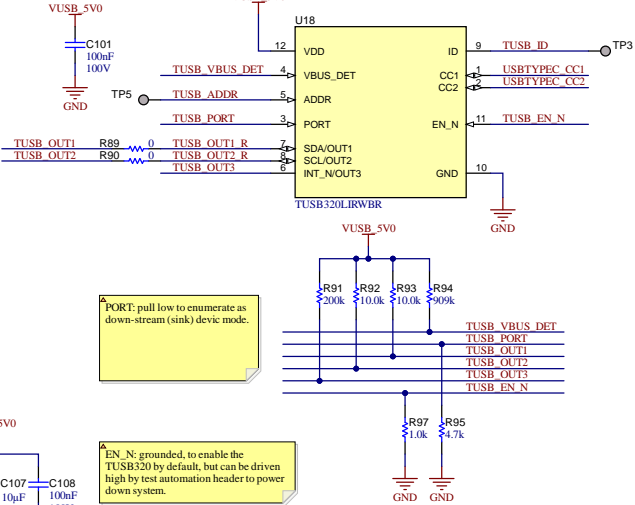
USB Type-C Power Input: 5.0V, 3.1A



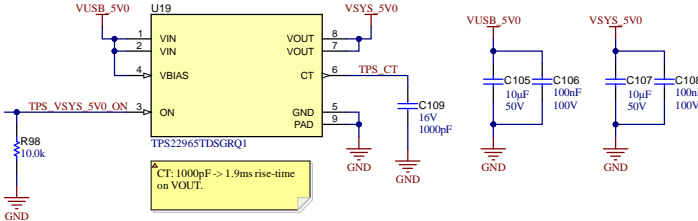
USB 5.0V Input Power Filtering



USB Type-C CC Logic Controller



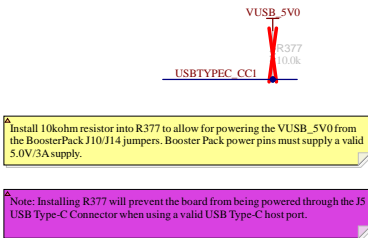
USB 5.0V Input Power Load Switch (4A max)



PORT: pull low to enumerate as down-stream (sink) device mode.

EN_N: grounded, to enable the TUSB320 by default, but can be driven high by test automation header to power down system.

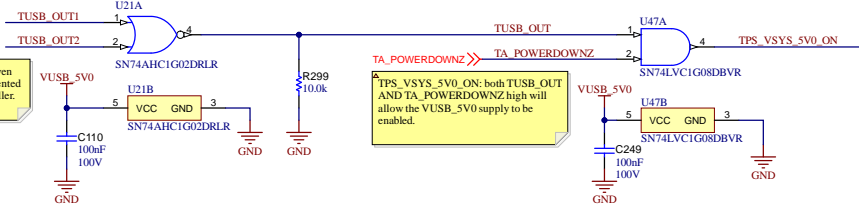
USB Type-C DFP CC Emulation



Install 10kohm resistor into R377 to allow for powering the VBUS_5V0 from the BoosterPack J10/J14 jumpers. Booster Pack power pins must supply a valid 5.0V/3A supply.

Note: Installing R377 will prevent the board from being powered through the J5 USB Type-C Connector when using a valid USB Type-C host port.

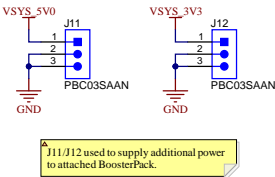
Input Power Load Switch Enable



TUSB_OUT[2:1]: both driven low when a 3A source presented to the TUSB320 CC controller.

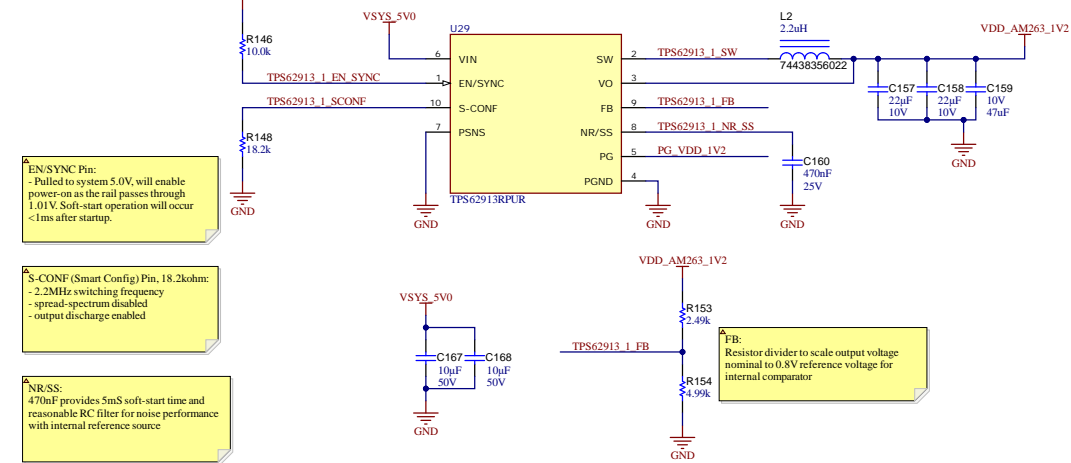
TPS_VSYS_5V0_ON: both TUSB_OUT AND TA_POWERDOWNZ high will allow the VBUS_5V0 supply to be enabled.

Boosterpack Extended Power

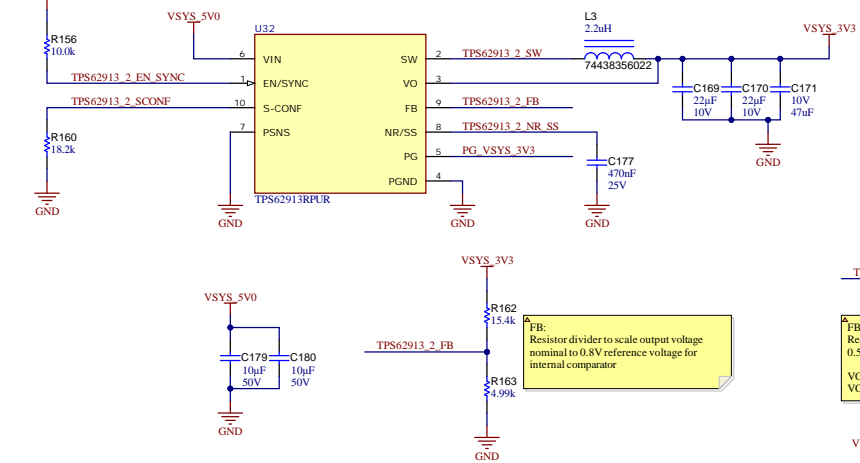


J11/J12 used to supply additional power to attached BoosterPack.

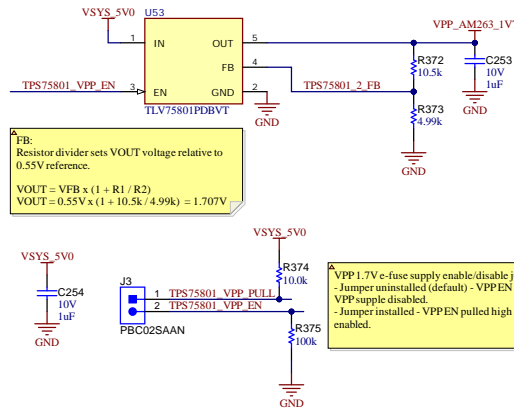
AM263x Core Digital 1.2V, 3A



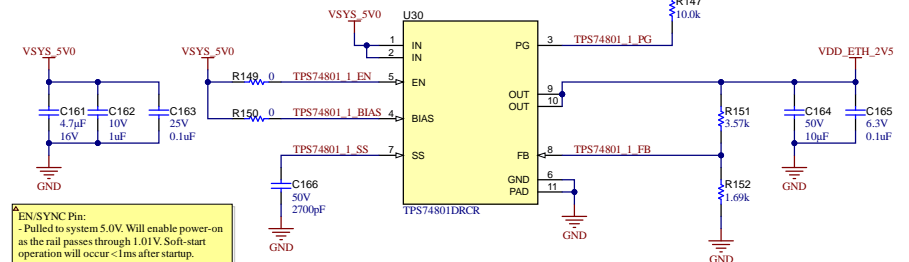
System 3.3V, 3A



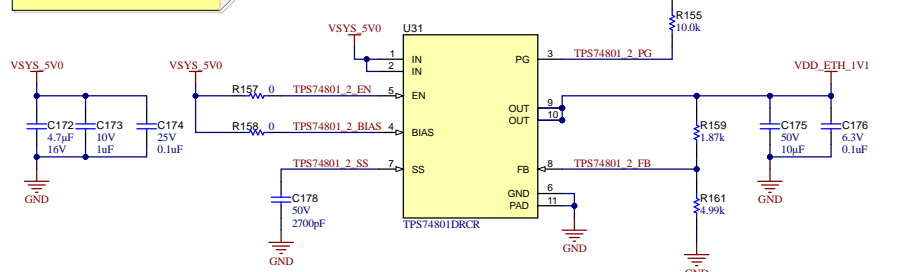
VPP E-Fuse 1.7V, 500mA



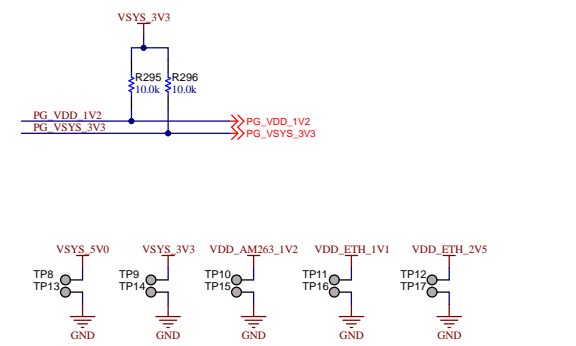
Ethernet PHY 2.5V, 1.5A

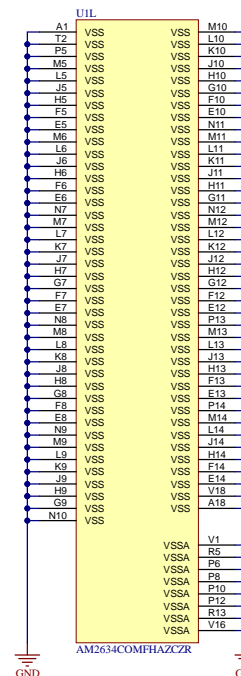
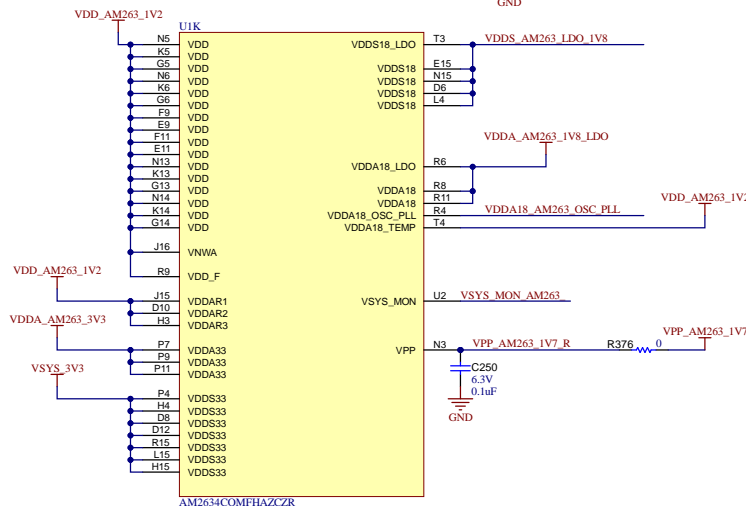
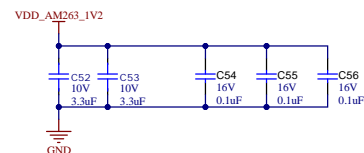
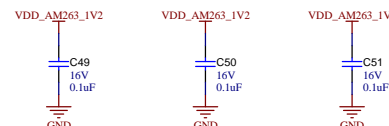
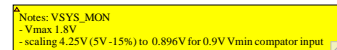
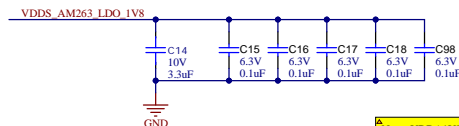
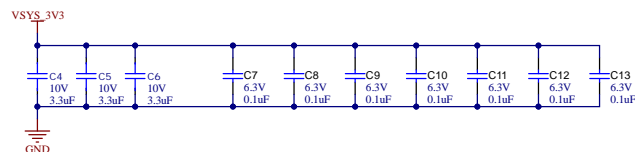
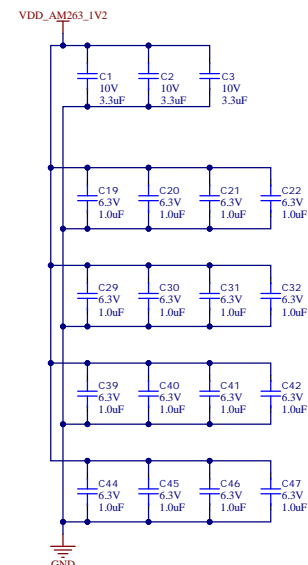


Ethernet PHY 1.1V, 1.5A



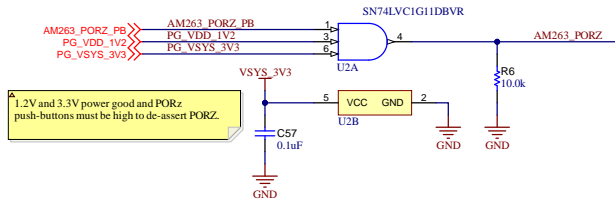
1.2V, 3.3V Power-Good



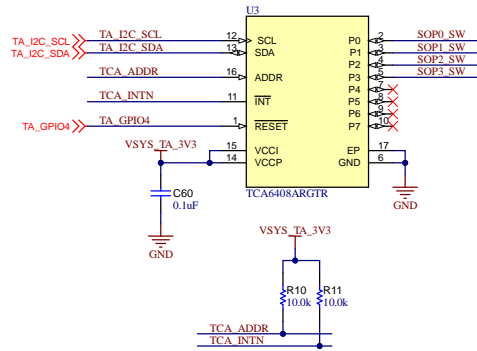


AM263x Clock, Reset, Boot, JTAG

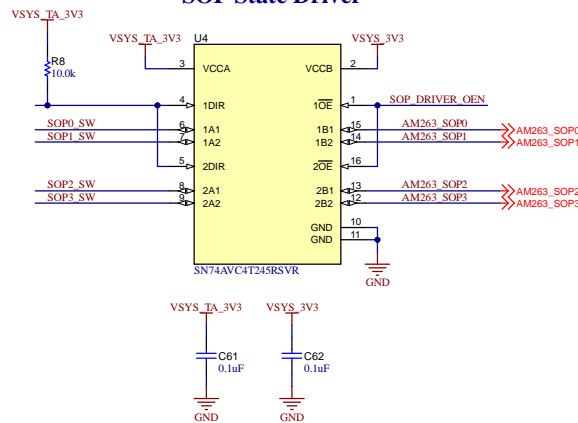
POR Generation



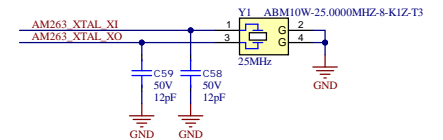
Test Automation SOP Select



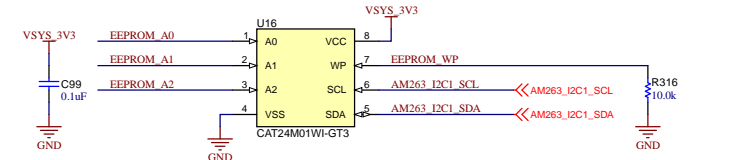
SOP State Driver



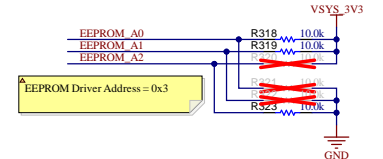
25 MHz Crystal



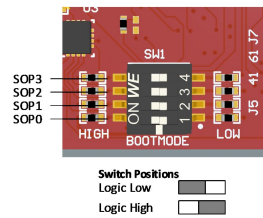
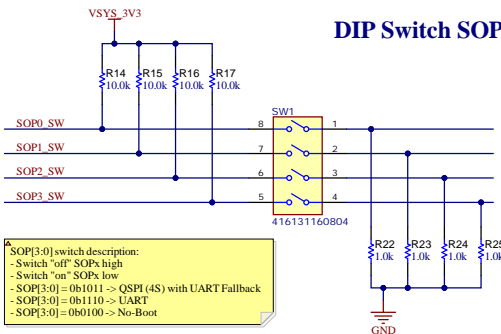
Board ID EEPROM



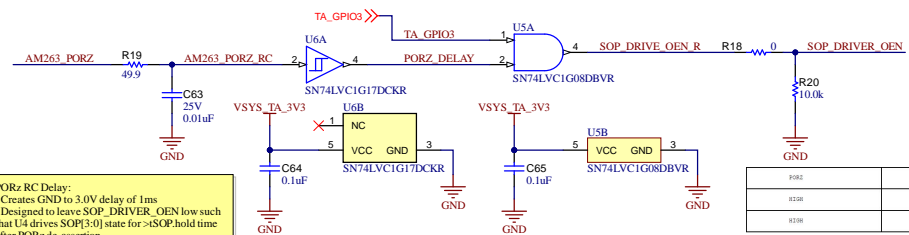
EEPROM Address



DIP Switch SOP Select



PORZ SOP Driver RC Delay

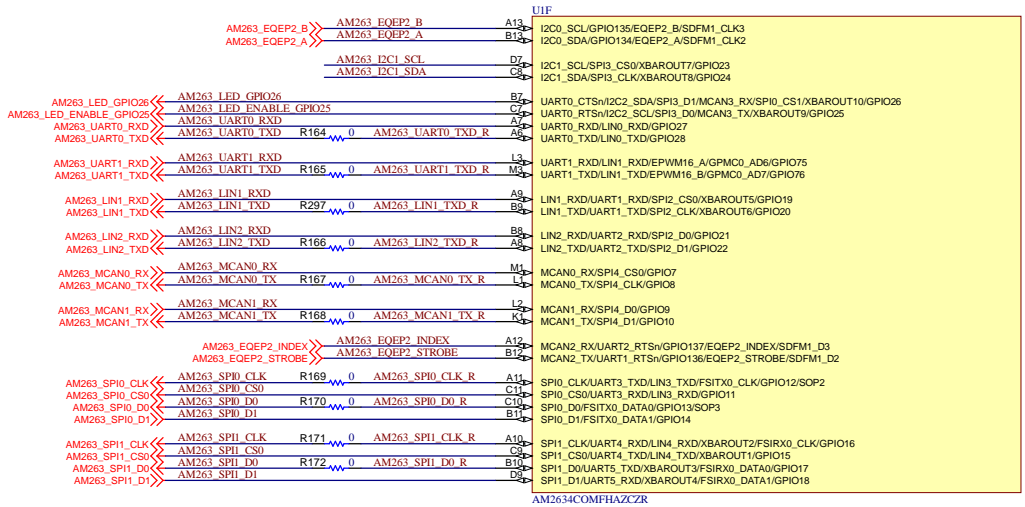


PORz RC Delay:
- Creates GND to 3.0V delay of 1ms
- Designed to leave SOP_DRIVER_OEN low such that U4 drives SOP[3:0] state for >SOP hold time after PORz de-assertion

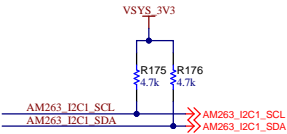
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Orderable: LP-AM263	Designed for:	Mod. Date: 11/30/2024
TID #: N/A	Project Title: AM263x Launchpad	
Number: PROC111	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 5 of 24
Drawn By: a0271760	File: PROC111_AM263x_2_Clock_Reset_Boot_JTAG_SchBoc	http://www.ti.com
Engineer: a0271760	Contact:	© Texas Instruments

AM263x Serial Connectivity



I2C1 Pull-Up




AM263x SOP[3:2]



Layout Note: 10kOhm resistors placed near device to AM263x device with common pad inline with SPI path. This will help isolate SPI path from SOP stub path during QSPI usage of these pins. No additional stubs should be formed.

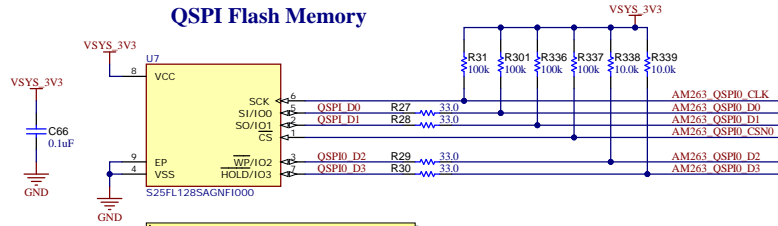
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Orderable: LP-AM263	Designed for:	Mod. Date: 11/30/2024	 TEXAS INSTRUMENTS
TID #: N/A	Project Title: AM263x Launchpad		
Number: PROC111	Rev: A	Sheet Title: XDS110 JTAG/USB-to-UART Bridge	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 6 of 24	
Drawn By: a0271760	File: PROC111_AM263x_Serial_Connectivity.SchDoc	Size: B	
Engineer: a0271760	Contact:		http://www.ti.com
			© Texas Instruments

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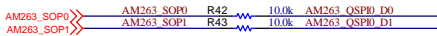
AM263x QSPI and MMC

QSPI Flash Memory



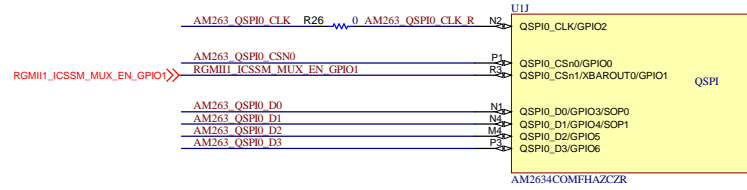
Note: Package compatible alternate flash memories:
- GD25B64CWAG

AM263x SOP[1:0]

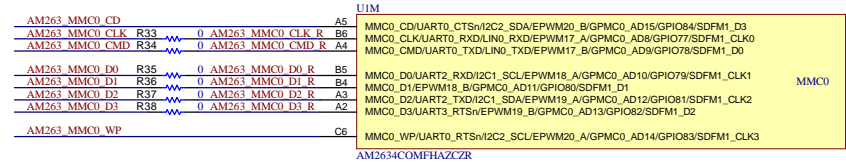


Layout Note: 10kohm resistors placed near device to Flash device with common pad inline with QSPI path. This will help isolate QSPI path from SOP stub path during QSPI usage of these pins. No additional stubs should be formed.

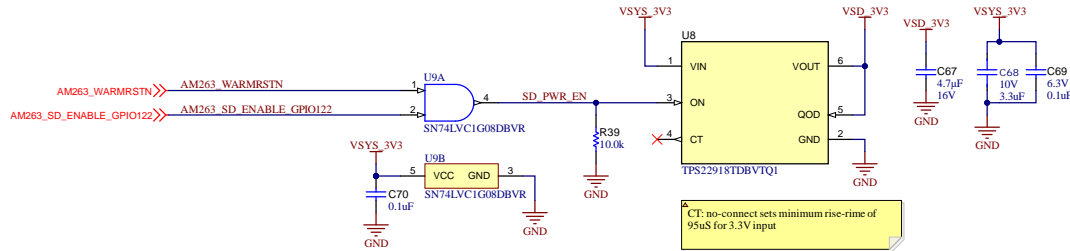
AM263x QSPI0



AM263x MMC0

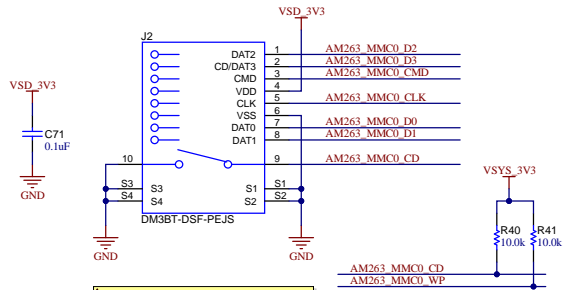


Micro-SD Power Switch



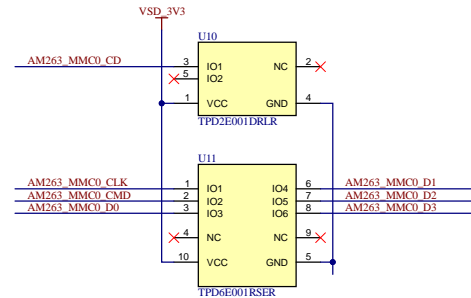
CT: no-connect sets minimum rise-time of 95uS for 3.3V input

Micro-SD Card Socket




Pin 9, 10 are the card-detect switch:
- card inserted: 9 and 10 shorted
- card removed: 9 and 10 open

Micro-SD ESD

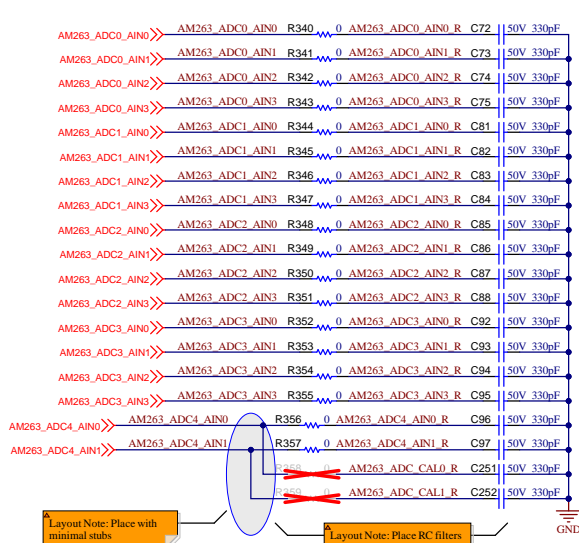


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Orderable: LP-AM263	Designed for:	Mod. Date: 11/30/2024	 TEXAS INSTRUMENTS http://www.ti.com	
TID #: N/A	Project Title: AM263x Launchpad			
Number: PROC111	Rev: A	Sheet Title:		
SVN Rev: Not in version control		Assembly Variant: 001		Sheet: 7 of 24
Drawn By: ao271760		File: PROC111_AM263x_3_GSPI_MMC_SchDoc		Size: B
Engineer: ao271760	Contact:		© Texas Instruments	

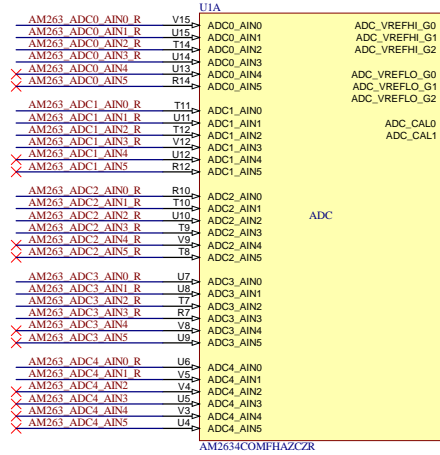
AM263x ADC and DAC

SAR ADC RC Filtering

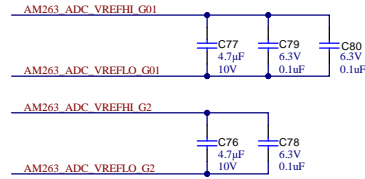


Layout Note: Place with minimal stubs

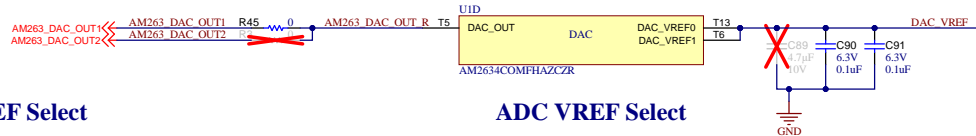
Layout Note: Place RC filters with minimal distance between components and close to MCU BGA.



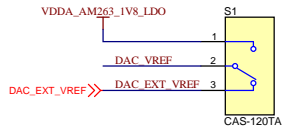
ADC VREF Decoupling



DAC Output, VREF

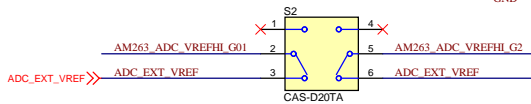


DAC VREF Select



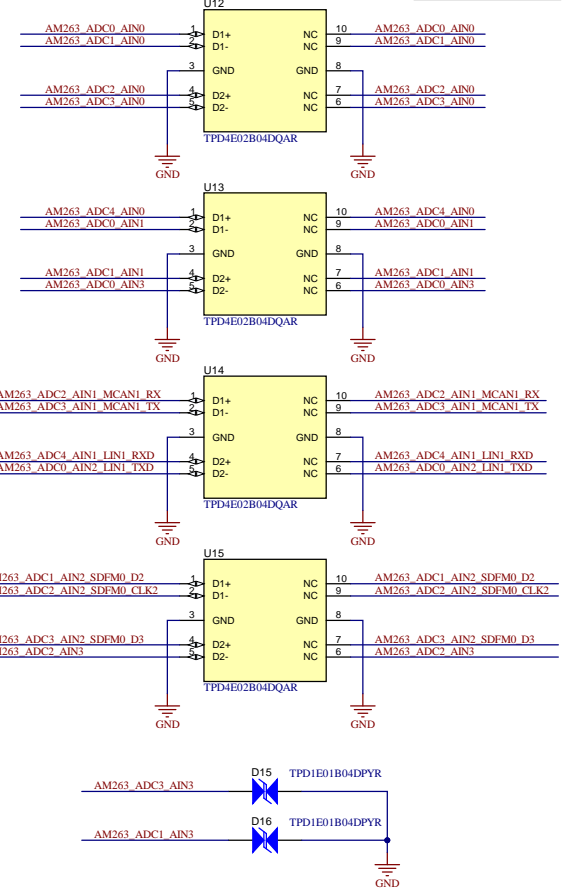
DAC VREF Switch Select - 1.8V VREF must be provided for AM263x comparators to function
- Select pins 1-2 select AM263x 1.8V analog LDO output as DAC VREF
- Select pins 2-3 select external 1.8V VREF (default)

ADC VREF Select



ADC VREF Switch Select
- Switch in 1-2 position allows AM263x on-die ADC VREF (default) for VREFG0/G1
- Switch in 2-3 position selects external 1.8V VREF (if any provided) for VREFG0/G1
- Switch in 4-5 position allows AM263x on-die ADC VREF (default) for VREFG2
- Switch in 5-6 position selects external 1.8V VREF (if any provided) for VREFG2

DAC Output, VREF

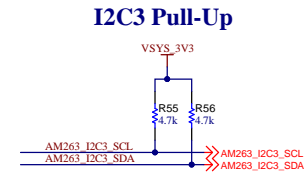
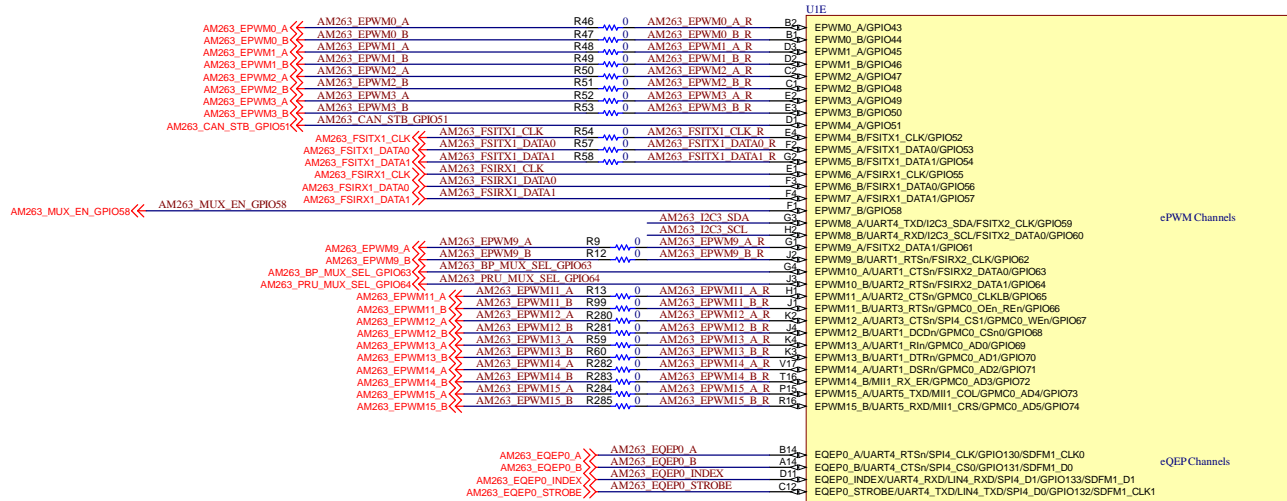


Layout Note: Place external ESD near connectors

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TID #: N/A	Project Title: AM263x Launchpad	
Number: PROC111	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 8 of 24
Drawn By: a0271760	File: PROC111_AM263x_4_DAC_ADC_SchDoc	Size: B
Engineer: a0271760	Contact:	

AM263x ePWM, eQEP, FSI

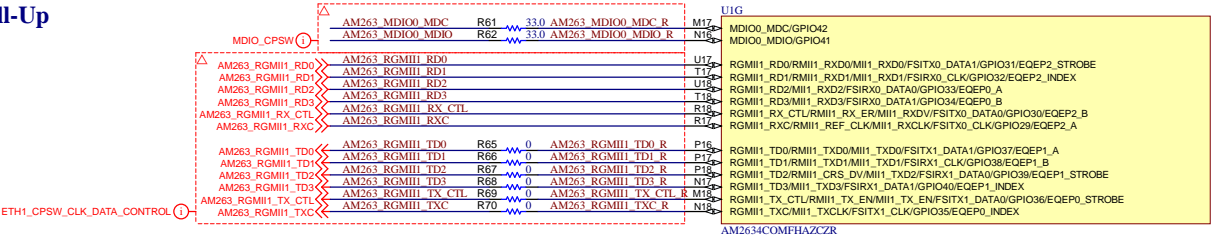
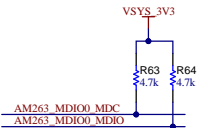


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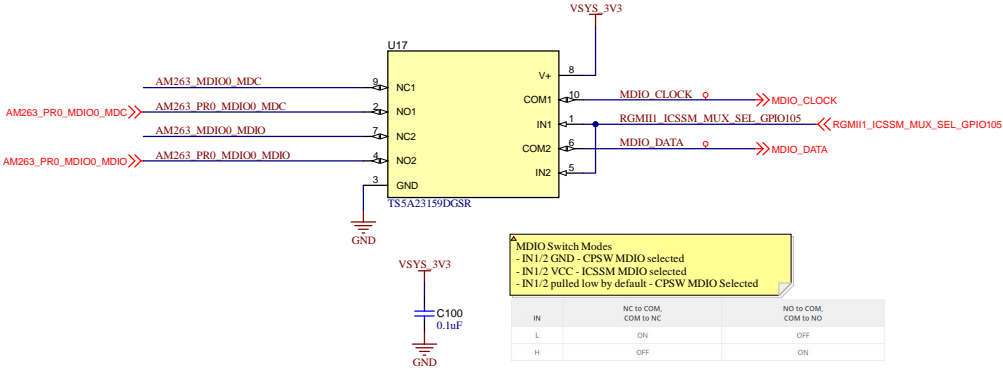
Orderable: LP-AM263	Designed for: AM263x Launchpad	Mod. Date: 11/30/2024
TID #: N/A	Project Title: AM263x Launchpad	
Number: PROC111	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 9 of 24
Drawn By: a0271760	File: PROC111_AM263x_5_ePWM_eQEP_FSI_Sch16	Size: B
Engineer: a0271760	Contact:	http://www.ti.com

AM263x CPSW - RGMII1 and MDIO

CPSW MDIO Pull-Up



CPSW/ICSSM MDIO Switch

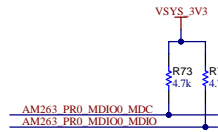


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Orderable: LP-AM263	Designed for: AM263x Launchpad	Mod. Date: 11/30/2024
TID #: N/A	Project Title: AM263x Launchpad	
Number: PROC111	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 10 of 24
Drawn By: a0271760	File: PROC111_AM263x_6_RGMII_MII_SchDoc	Size: B
Engineer: a0271760	Contact:	

AM263x PR0 PRU0 and PRU1

ICSSM MDIO Pull-Up



ETH2_ICSSM_CLK_DATA_CONTROL

ETH1_ICSSM_CLK_DATA_CONTROL

ETH1_CLK_DATA_CONTROL

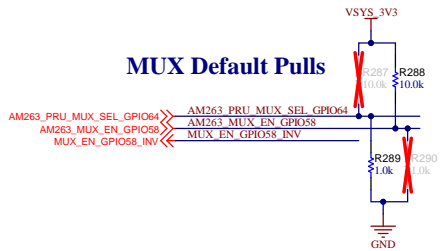
ETH1_ICSSM_CLK_DATA_CONTROL

IEP_EDIO_SYNC

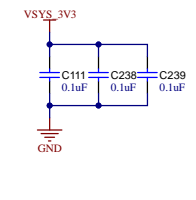
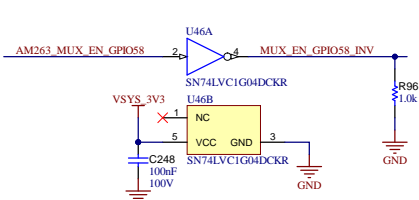
RGMII1_ICSSM_MUX_SEL_GPIO105

EN	SEL1	SEL2	FUNCTION
L	X	X	A ₀ to A ₁₁ , B ₀ to B ₁₁ , and C ₀ to C ₁₁ are Hi-Z
H	L	L	A ₀ to A ₉ = B ₀ to B ₉ and A ₁₀ to A ₁₁ = B ₁₀ to B ₁₁
H	L	H	A ₀ to A ₉ = B ₀ to B ₉ and A ₁₀ to A ₁₁ = C ₀ to C ₁₁
H	H	L	A ₀ to A ₉ = C ₀ to C ₉ and A ₁₀ to A ₁₁ = B ₁₀ to B ₁₁
H	H	H	A ₀ to A ₉ = C ₀ to C ₉ and A ₁₀ to A ₁₁ = C ₁₀ to C ₁₁

MUX Default Pulls

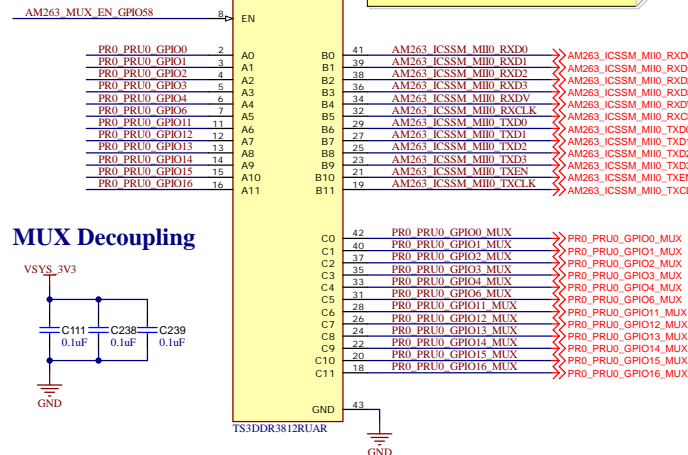


MUX Decoupling

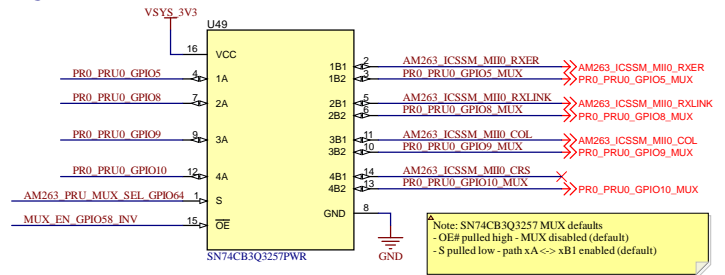


PR0_PRU0 PHY #2 / Boosterpack MUX

Note: TS3DDR MUX defaults
- EN# pulled high - MUX disabled (default)
- SEL# pulled low - path A <-> B enabled (default)



MUX Decoupling



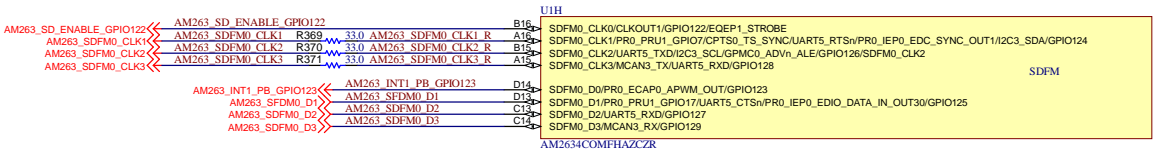
INPUTS	OUTPUT	FUNCTION
OE	S	
L	L	B1
L	H	B2
H	X	Z

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Orderable: LP-AM263	Designed for:	Mod. Date: 11/30/2024
TID #: N/A	Project Title: AM263x Launchpad	
Number: PROC111	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 11 of 24
Drawn By: a0271760	File: PROC111_AM263x_7_PRU_SchDoc	Size: B
Engineer: a0271760	Contact:	




AM263x SDFM



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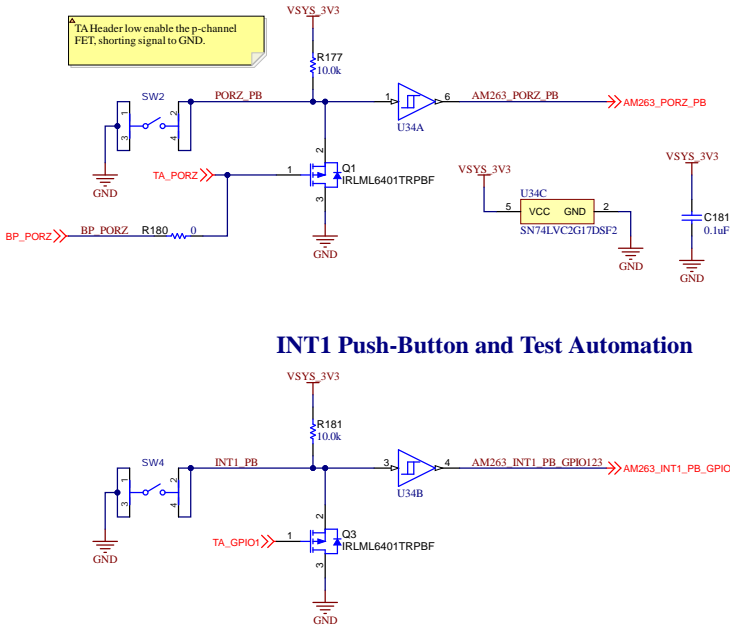
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TID #: N/A	Project Title: AM263x Launchpad	
Number: PROC111	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 12 of 24
Drawn By: a0271760	File: PROC111_AM263x_8_SDFM.SchDoc	Size: B
Engineer: a0271760	Contact:	

 **TEXAS
INSTRUMENTS**

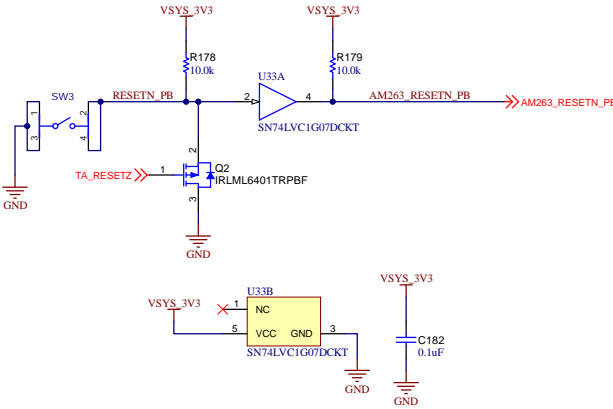
http://www.ti.com
© Texas Instruments

Push-Buttons

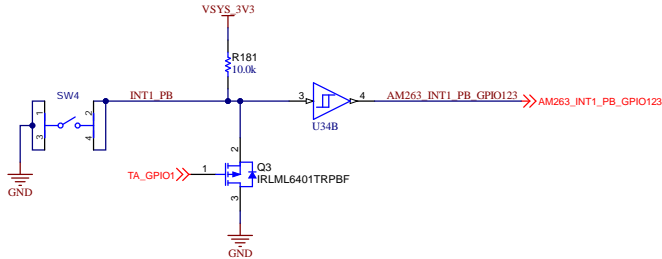
PORZ Push-Button and Test Automation



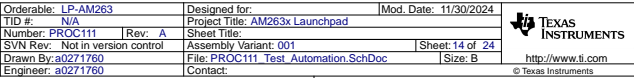
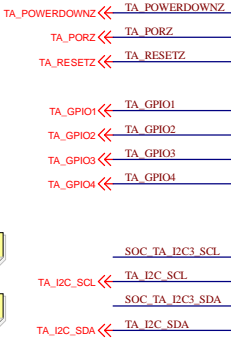
RESETZ Push-Button and Test Automation



INT1 Push-Button and Test Automation



Pushbuttons and Test Automation Header

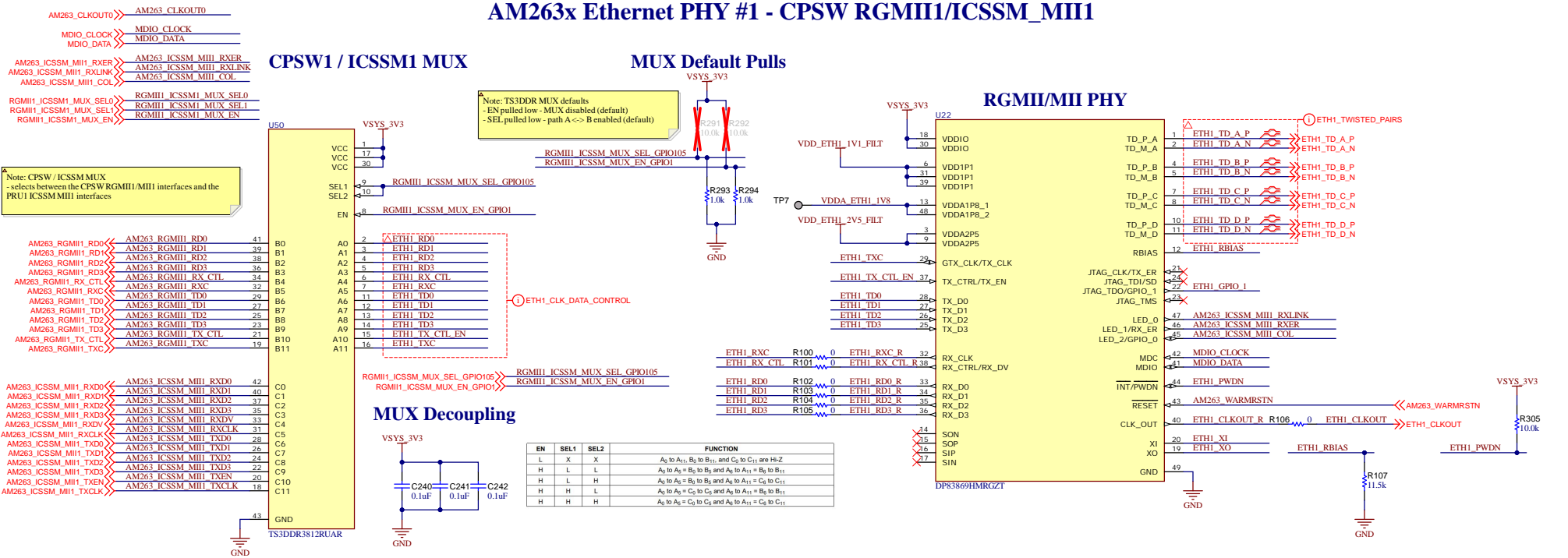


AM263x Ethernet PHY #1 - CPSW RGMII1/ICSSM_MII1

CPSW1 / ICSSM1 MUX

MUX Default Pulls

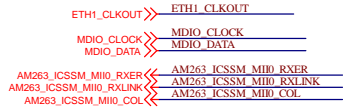
RGMII/MII PHY



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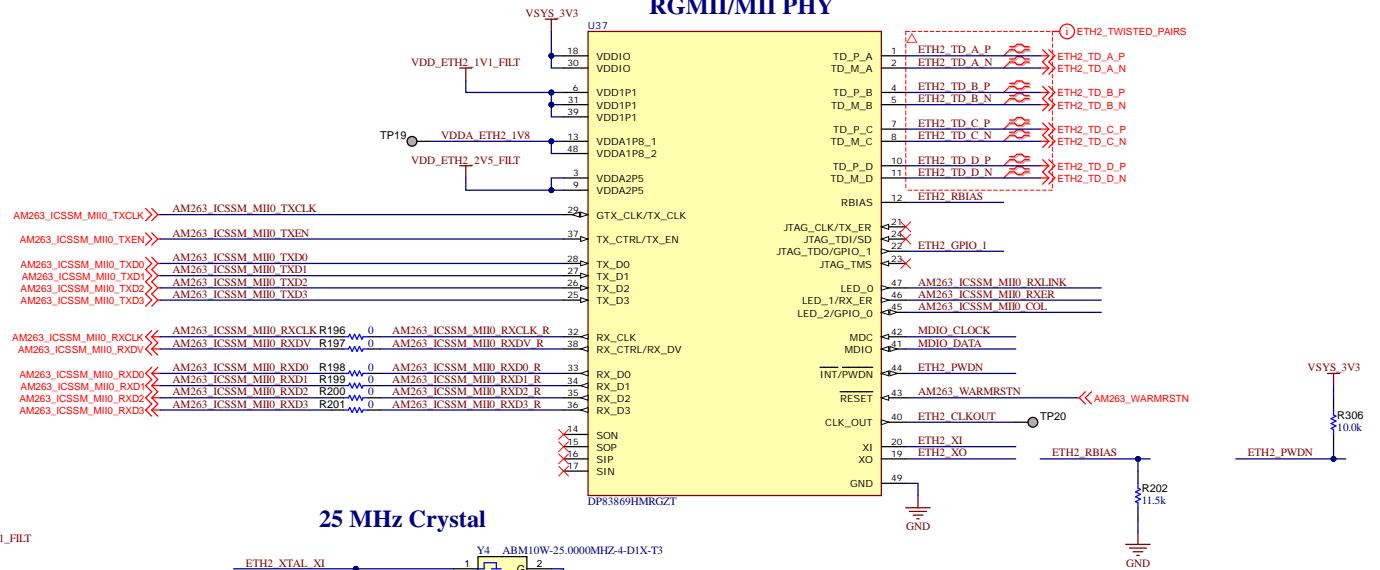
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Number: PROC111	Rev: A	Assembly Variant: 001
SVN Rev: Not in version control	File: PROC111_Ethernet_PHY_1_SchDoc	Sheet: 15 of 24
Drawn By: a0271760	Engineer: a0271760	Contact:
Engineer: a0271760		

AM263x Ethernet PHY #2 - CPSW RGMII2/ICSSM_MII0

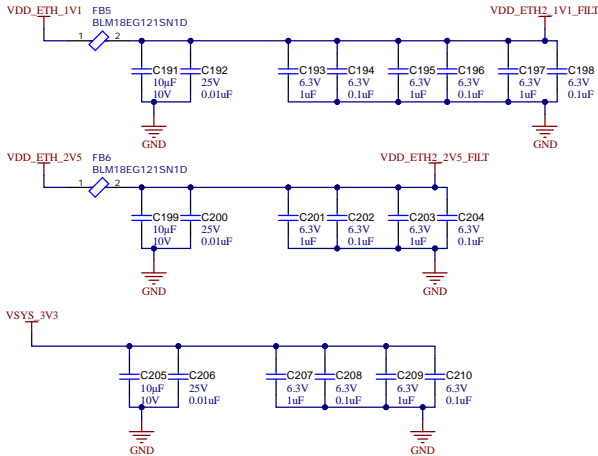


Note: CPSW / ICSSM Pinmux On AM263x
- selects between the CPSW RGMII2/MII2 interfaces and the PRU1 ICSSM MII0 interfaces

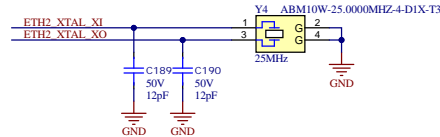
RGMII/MII PHY



PHY Decoupling



25 MHz Crystal

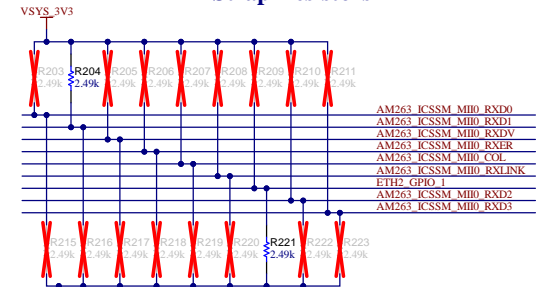


Oscillator Input Selection



Note: PHY Oscillator Input Selection
- (default) select 25 MHz XTAL input to ETH2_XI/SO
- optional select to disable 25 MHz XTAL and input CLK_OUT from PHY1

PHY Strap Resistors



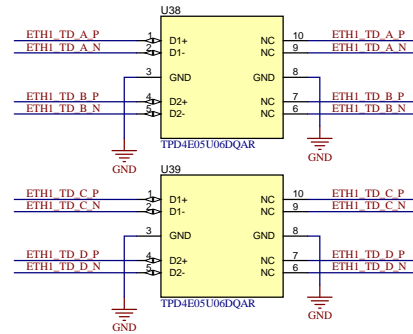
Note: PHY Strap Resistor Selection
- PHY_ADDRESS = 0b1100_00C
- Auto-negotiation, 10/100/1000 advertised, Auto-MDIX
- RGMII to Copper (1000Base-T/100Base-TX/10Base-Tc)

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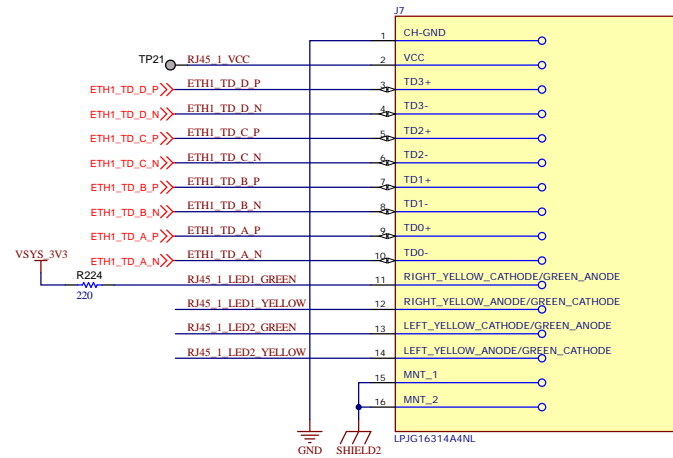
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Number: PROC111	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 16 of 24
Drawn By: a0271760	File: PROC111_Ethernet_PHY_2_SchDoc	Size: B
Engineer: a0271760	Contact:	

AM263x Ethernet PHY #1 - RJ-45 Jack

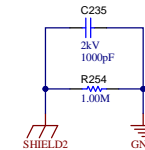
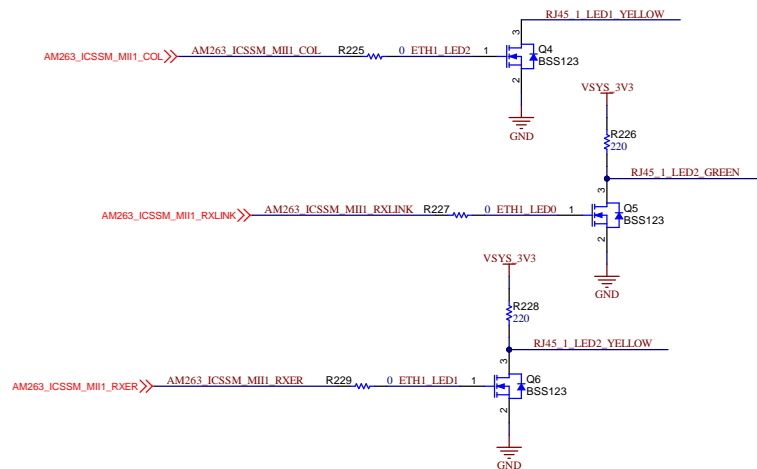
RJ-45 ESD Protection



RJ-45 Jack #1



RJ-45 LED Drivers

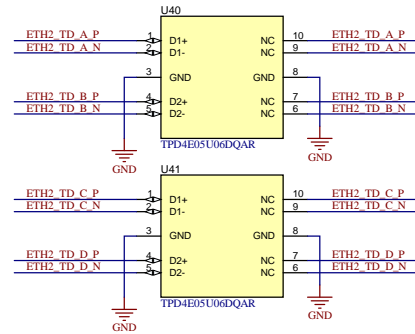


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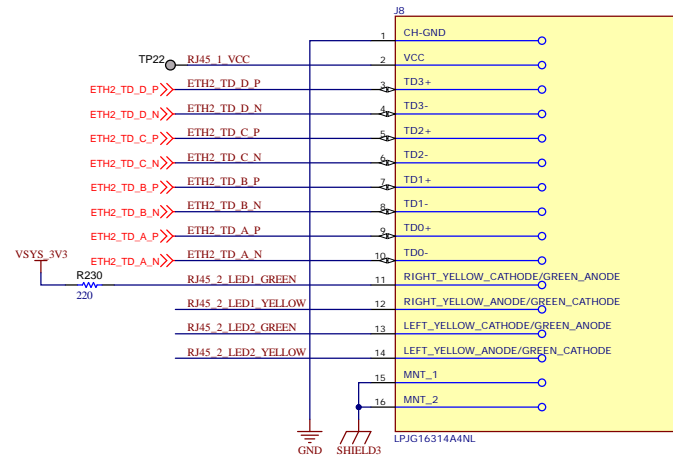
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Number: PROC111	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 17 of 24
Drawn By: a0271760	File: PROC111_Ethernet_RJ45_1.SchDoc	Size: B
Engineer: a0271760	Contact:	

AM263x Ethernet PHY #2 - RJ-45 Jack

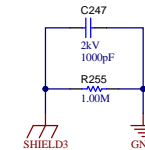
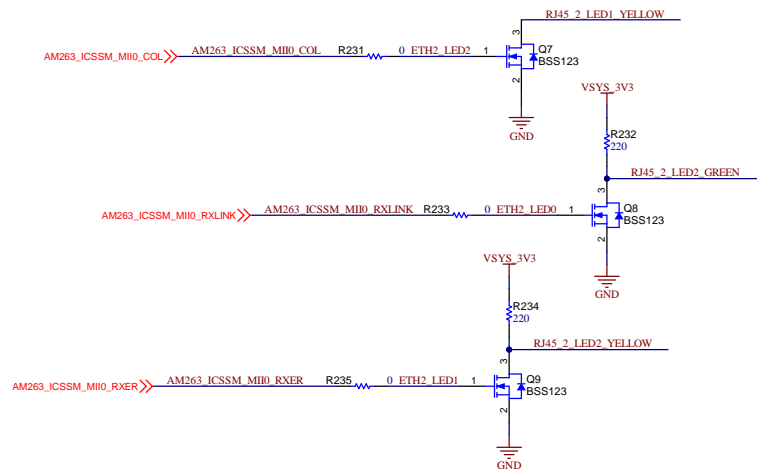
RJ-45 ESD Protection



RJ-45 Jack #2



RJ-45 LED Drivers

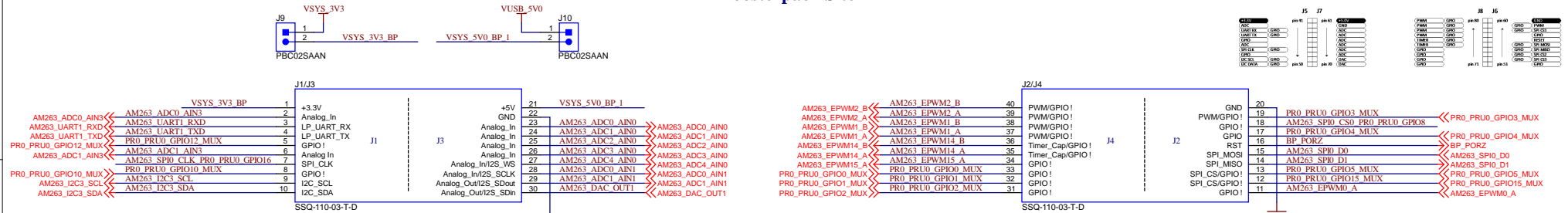


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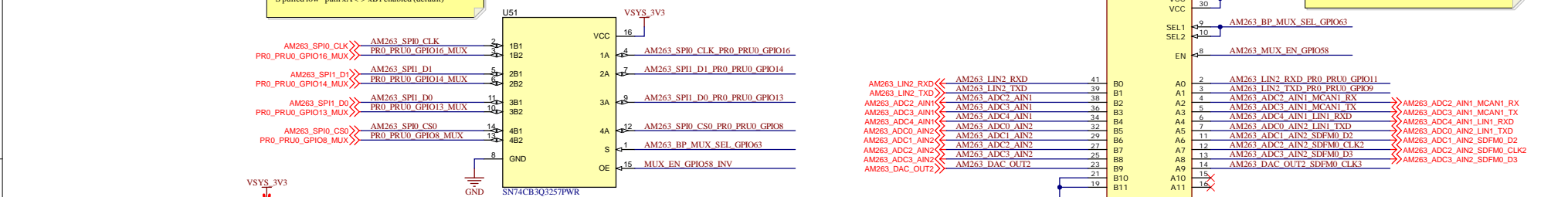
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Drawn By: a0271760	File: PROC111_Ethernet_RJ45_2_SchDoc	Size: B
Engineer: a0271760	Contact:	

Boosterpack Headers

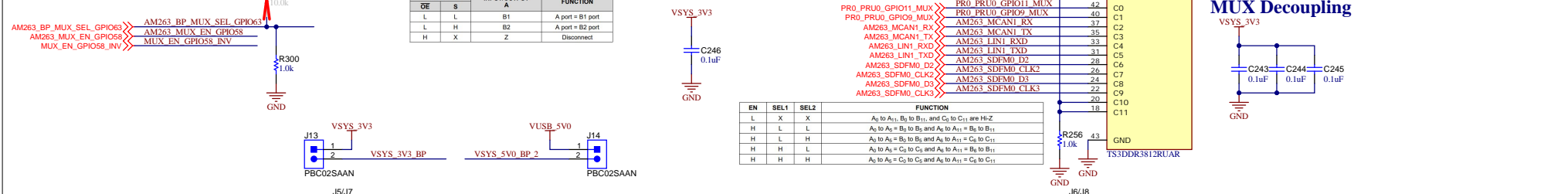
Boosterpack Site 1



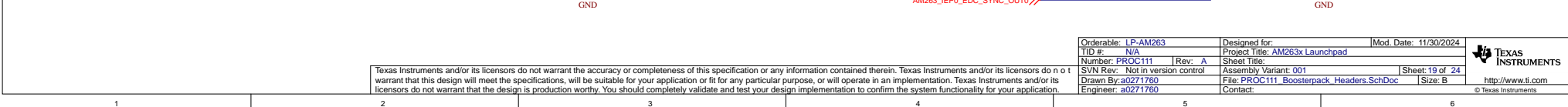
Alternate Boosterpack Function MUX



MUX Decoupling



Boosterpack Site 2



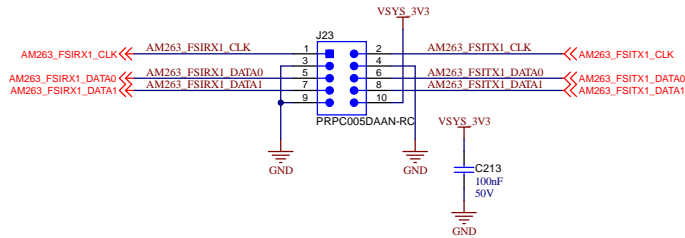
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TID #: N/A	Sheet Title: Assembly Variant: 001	Sheet: 19 of 24
Number: PROC111	Rev: A	File: PROC111_Boosterpack_Headers.SchDoc
SVN Rev: Not in version control	Drawn By: a0271760	Engineer: a0271760
		Contact:

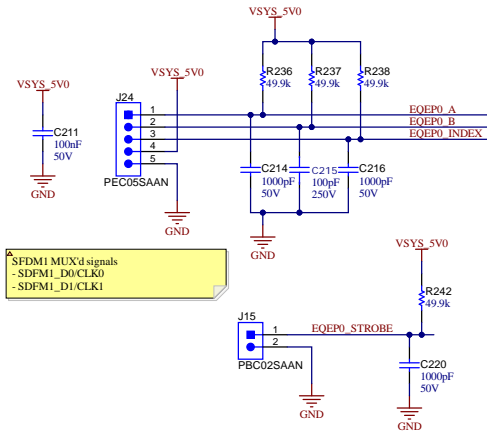
Breakout Headers

FSI Header

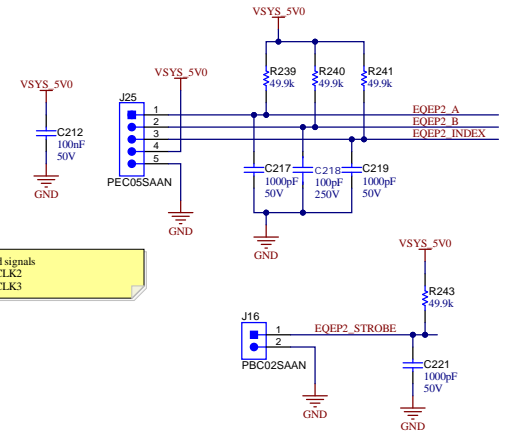
C2000 LP Style FSI Breakout



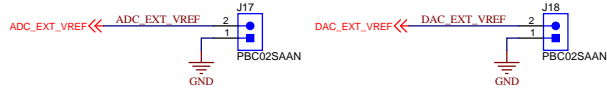
eQEP0/SFDM1 Headers



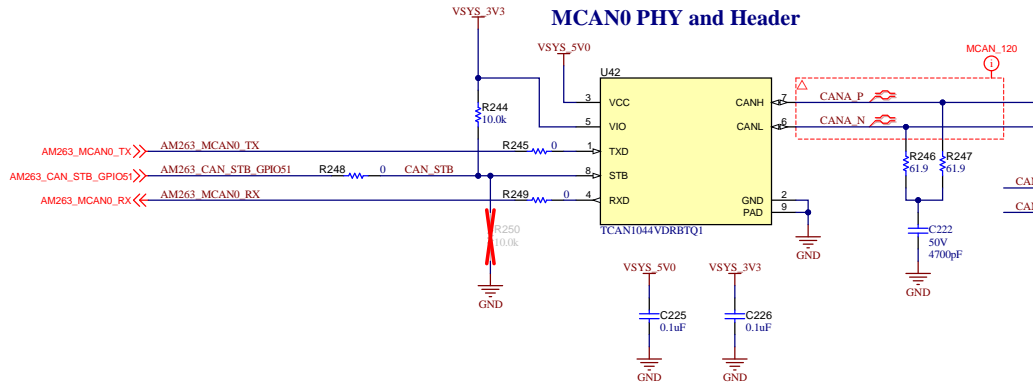
eQEP2/SFDM2 Headers



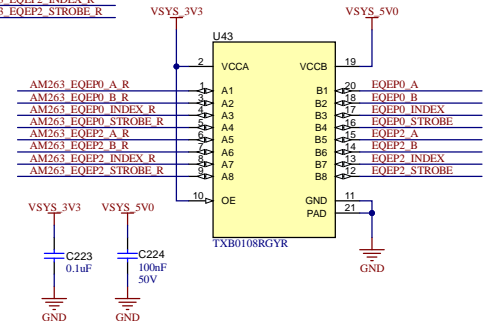
ADC/DAC External VREF Header



MCAN0 PHY and Header



eQEP Bi-Directional Level Translator

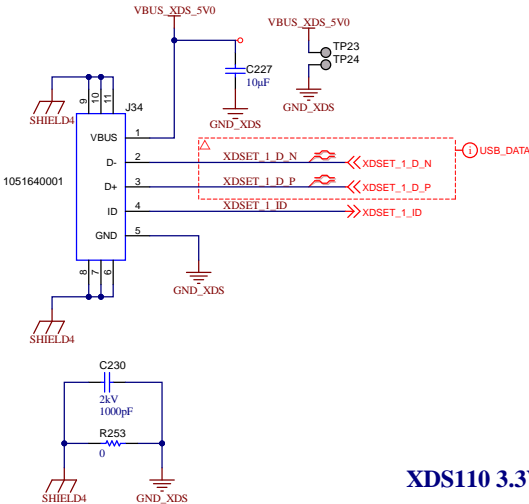


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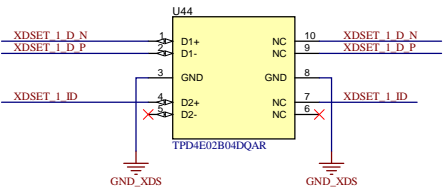
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SVN Rev: Not in version control	Drawn By: a0271760	Engineer: a0271760
Assembly Variant: 001	File: PROC111_Breakout_Headers.SchDoc	Contact:
Sheet: 20 of 24	Size: B	

XDS110 JTAG/USB-to-UART Bridge

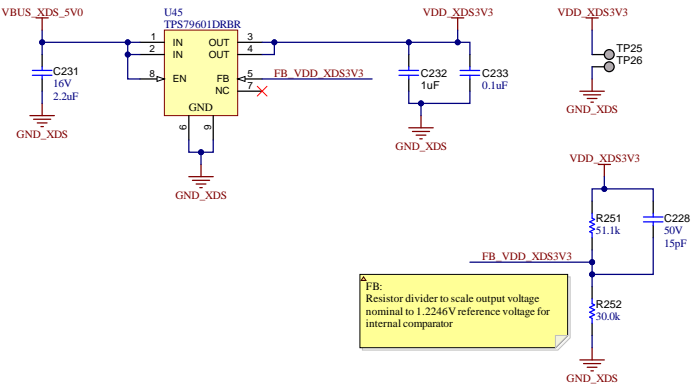
XDS110 USB Micro-B PORT



USB Mini-B ESD Protection



XDS110 3.3V LDO

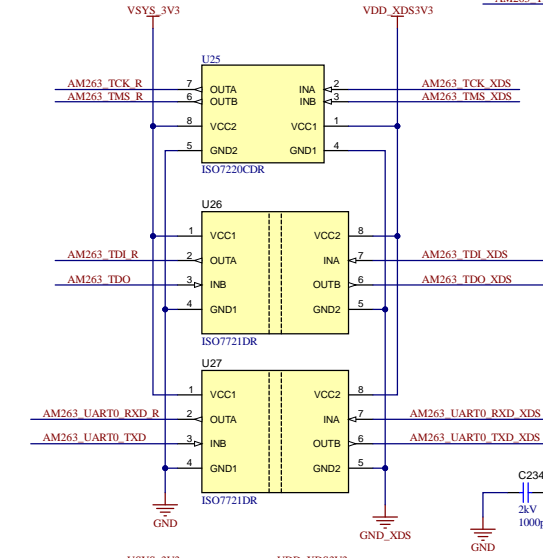
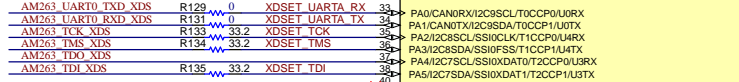
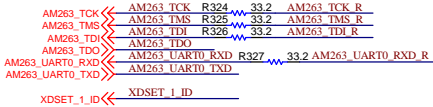


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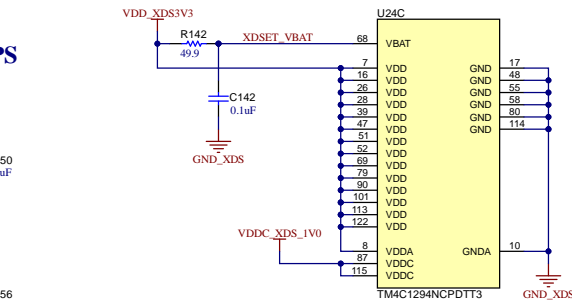
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TID #: N/A	Project Title: XDS110 JTAG/USB-to-UART Bridge	
Number: PROC111	Rev: A	Sheet: 21 of 24
SVN Rev: Not in version control	Assembly Variant: 001	Size: B
Drawn By: a0271760	File: PROC111_XDS110_1.SchDoc	
Engineer: a0271760	Contact:	

TM4C1294NCPDT Datasheet
XDS110 Collateral

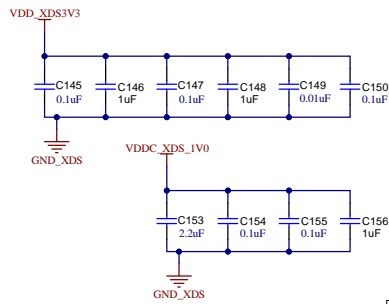
XDS110 JTAG/USB-to-UART Bridge



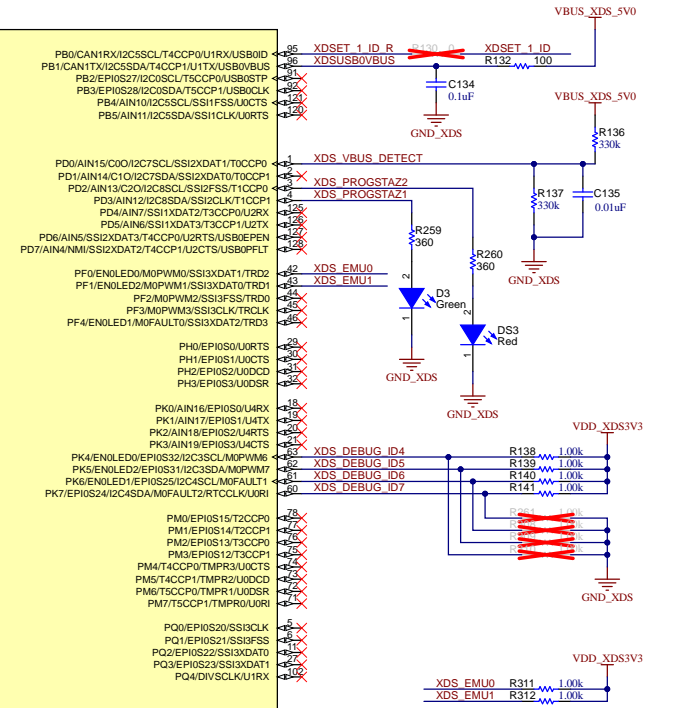
Note: C234 shorts GND_XDS and GND. This should be done close to the ISO7721 components, bridging the



XDS110 DECOUPLING CAPS

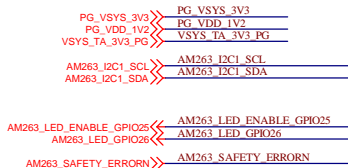


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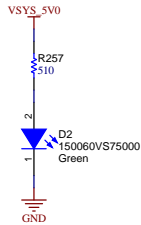


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Number: PROC111	Rev: A	Assembly Variant: 001
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Drawn By: a0271760	Engineer: a0271760	Contact:

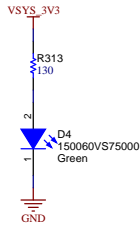
System LED Indicators



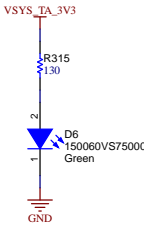
System 5.0V



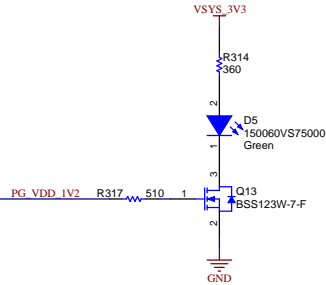
System 3.3V



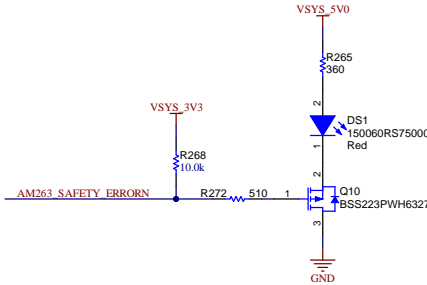
Test Automation 3.3V



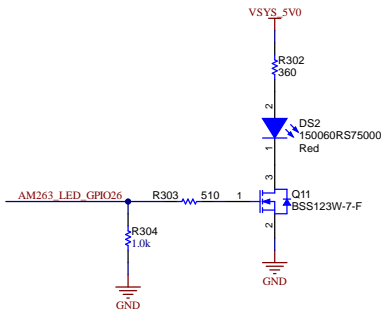
AM263x 1.2V



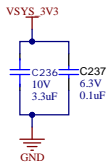
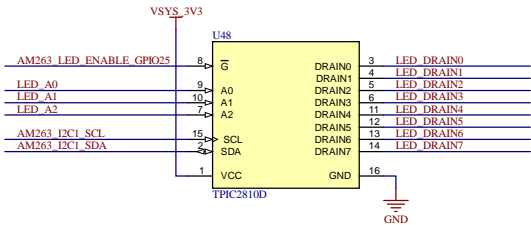
AM263x Safety Error



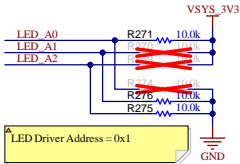
AM263x GPIO LED



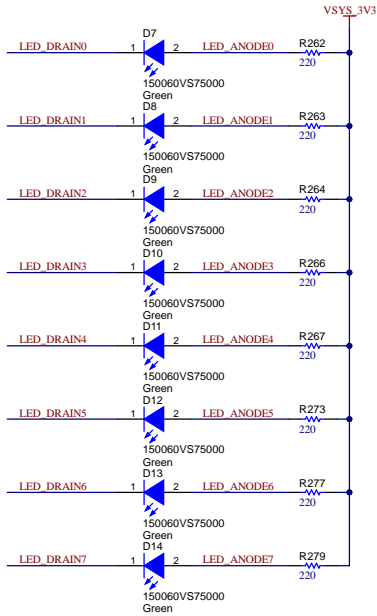
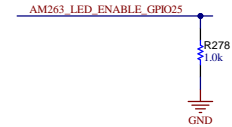
Industrial LED Driver



LED Driver Address



LED Driver Enable



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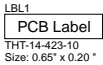
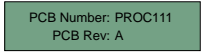
Orderable: LP-AM263	Designed for:	Mod. Date: 11/30/2024
TID #: N/A	Project Title: AM263x Launchpad	
Number: PROC111	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 23 of 24
Drawn By: a0271760	File: PROC111_LED.SchDoc	Size: B
Engineer: a0271760	Contact:	http://www.ti.com

System Hardware, Notes, Labels

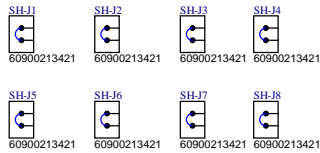
PCB Fiducials



PCB Labels and Silkscreen



Included Jumpers



PCB Labels and Silkscreen

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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Orderable: LP-AM263	Designed for:	Mod. Date: 11/30/2024
TID #: N/A	Project Title: AM263x Launchpad	
Number: PROC111	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 24 of 24
Drawn By: a0271760	File: PROC111_Hardware_SchDoc	Size: B
Engineer: a0271760	Contact:	

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